

**IN THE CLAIMS:**

Please amend claims 1, 7, 8, 11-18, 20, 26, 27, and 30, as set forth below.

1           1.       (Currently Amended) A method comprising:  
2     requesting a first deferred procedure call for a first interrupt event associated with a  
3           source;  
4     requesting at least one other different deferred procedure call for a second interrupt event  
5           associated with the source, wherein the first interrupt event comprises one type of  
6           event and the second interrupt event comprises another type of event;  
7     assigning the first deferred procedure call and the at least one other deferred procedure  
8           call to a resource;  
9     processing the first interrupt event with the first deferred procedure call; and  
10    processing the second interrupt event with the at least one other deferred procedure call.

1           2.       (Original) The method of claim 1, further comprising:  
2     assigning the first deferred procedure call and the at least one other deferred procedure  
3           call to a resource comprising a processor exhibiting a single thread of execution;  
4           and  
5     executing the first deferred procedure call and the at least one other deferred procedure  
6           call on the single thread.

1           3.       (Original) The method of claim 1, further comprising:  
2     assigning the first deferred procedure call and the at least one other deferred procedure  
3           call to a resource comprising a processor exhibiting a plurality of threads; and  
4     executing the first deferred procedure call on one thread of the plurality of threads while  
5           executing the at least one other deferred procedure call on another thread of the  
6           plurality of threads.

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1           4.       (Original) The method of claim 1, further comprising:  
2     assigning the first deferred procedure call to a resource comprising a first thread of a  
3           processor;  
4     assigning the at least one other deferred procedure call to a resource comprising a second  
5           thread of the processor; and  
6     executing the first deferred procedure call on the first thread while executing the at least  
7           one other deferred procedure call on the second thread.

1           5.       (Original) The method of claim 1, further comprising:  
2     assigning the first deferred procedure call and the at least one other deferred procedure  
3           call to a resource comprising a multi-processor system; and  
4     executing the first deferred procedure call on one processor of the multi-processor system  
5           while executing the at least one other deferred procedure call on another processor  
6           of the multi-processor system.

1           6.       (Original) The method of claim 1, further comprising:  
2     assigning the first deferred procedure call to a resource comprising a first processor;  
3     assigning the at least one other deferred procedure call to a resource comprising a second  
4         processor; and  
5     executing the first deferred procedure call on the first processor while executing the at  
6         least one other deferred procedure call on the second processor.

1           7.       (Currently Amended) The method of claim 1, further comprising  
2     processing ~~another~~ a third interrupt event associated with the source with the first  
3     deferred procedure call, the third interrupt event comprising a third type of event or the at  
4     ~~least one other deferred procedure call.~~

1           8.       (Currently Amended) A method comprising:  
2     requesting a first deferred procedure call for a first interrupt event associated with a  
3         source;  
4     requesting at least one other different deferred procedure call for a second interrupt event  
5         associated with the source, wherein the first interrupt event comprises one type of  
6         event and the second interrupt event comprises another type of event; and  
7     processing the first interrupt event with the first deferred procedure call while processing  
8         the second interrupt event with the at least one other deferred procedure call.

1           9.       (Original) The method of claim 8, further comprising:  
2     executing the first deferred procedure call on a first thread of a processor; and  
3     executing the at least one other deferred procedure call on a second thread of the  
4     processor.

1           10.      (Original) The method of claim 8, further comprising:  
2     executing the first deferred procedure call on a first processor; and  
3     executing the at least one other deferred procedure call on a second processor.

1           11.      (Currently Amended) The method of claim 8, further comprising  
2     processing ~~another~~ a third interrupt event associated with the source with the first  
3     deferred procedure call, the third interrupt event comprising a third type of event or the at  
4     ~~least one other deferred procedure call.~~

1           12.      (Currently Amended) A driver comprising:  
2     an interrupt handler to identify interrupt events associated with a source; ~~and~~  
3     a first deferred procedure call, the first deferred procedure call to process a first type of  
4     the interrupt events; and  
5     a second at least two deferred procedure calls call, each of the at least two second  
6     deferred procedure ~~calls~~ call to process ~~at least one~~ a second type of the interrupt  
7     events.

1           13.     (Currently Amended) The driver of claim 12, the interrupt handler to  
2     assign the ~~at least two~~ first and second deferred procedure calls to a resource for  
3     execution.

1           14.     (Currently Amended) The driver of claim 12, the interrupt handler to  
2     assign ~~one of the at least two~~ first deferred procedure ealls call to a first resource for  
3     execution and ~~another of the at least two~~ second deferred procedure ealls call to a second  
4     resource for execution.

1           15.     (Currently Amended) A computer system comprising:  
2     a driver stored in a memory of the computer system, the driver including  
3         an interrupt handler to identify interrupt events associated with a source; and  
4         a first deferred procedure call, the first deferred procedure call to process a first  
5             type of the interrupt events; and  
6         a second ~~at least two~~ deferred procedure ealls call, ~~each of the at least two~~ second  
7             deferred procedure ealls call to process ~~at least one~~ a second type of the  
8             interrupt events[[]];  
9     and  
10    a processor to execute the ~~at least two~~ the first and second deferred procedure calls.

1           16.     (Currently Amended) The computer system of claim 15, the interrupt  
2 handler to assign the ~~at least two~~ first and second deferred procedure calls to a single  
3 thread exhibited by the processor for execution.

1           17.     (Currently Amended) The computer system of claim 15, the interrupt  
2 handler to assign ~~[[a]]~~ the first of the at least two deferred procedure ~~ealls~~ call to one  
3 thread of the processor and ~~another of the at least two~~ second deferred procedure ~~ealls~~  
4 call to a second thread of the processor for execution.

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1           18.     (Currently Amended) The computer system of claim 15, the interrupt  
2 handler to assign ~~one of the first~~ at least two deferred procedure ~~ealls~~ call to the processor  
3 and ~~another of the second~~ at least two deferred procedure ~~ealls~~ call to a second processor  
4 for execution.

1           19.     (Previously Submitted) The computer system of claim 15, wherein the  
2 source comprises a peripheral device coupled with the computer system.

1           20.     (Currently Amended) An article of manufacture comprising:  
2           a machine accessible medium, the machine accessible medium providing instructions  
3           that, when executed by a machine, cause the machine to:  
4           request a first deferred procedure call for a first interrupt event associated with a  
5           source;  
6           request at least one other different deferred procedure call for a second interrupt  
7           event associated with the source, wherein the first interrupt event  
8           comprises one type of event and the second interrupt event comprises  
9           another type of event;  
10          assign the first deferred procedure call and the at least one other deferred  
11          procedure call to a resource;  
12          process the first interrupt event with the first deferred procedure call; and  
13          process the second interrupt event with the at least one other deferred procedure  
14          call.

1           21.     (Original) The article of claim 20, wherein the instructions, when  
2           executed, further cause the machine to:  
3           assign the first deferred procedure call and the at least one other deferred procedure call  
4           to a resource comprising a processor exhibiting a single thread of execution; and  
5           execute the first deferred procedure call and the at least one other deferred procedure call  
6           on the single thread.

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1           22.     (Original) The article of claim 20, wherein the instructions, when  
2     executed, further cause the machine to:  
3     assign the first deferred procedure call and the at least one other deferred procedure call  
4           to a resource comprising a processor exhibiting a plurality of threads; and  
5     execute the first deferred procedure call on one thread of the plurality of threads while  
6           executing the at least one other deferred procedure call on another thread of the  
7           plurality of threads.

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1           23.     (Previously Submitted) The article of claim 20, wherein the instructions,  
2     when executed, further cause the machine to:  
3     assign the first deferred procedure call to a resource comprising a first thread of a  
4           processor;  
5     assign the at least one other deferred procedure call to a resource comprising a second  
6           thread of the processor; and  
7     execute the first deferred procedure call on the first thread while executing the at least  
8           one other deferred procedure call on the second thread.



1           24.     (Original) The article of claim 20, wherein the instructions, when  
2     executed, further cause the machine to:  
3     assign the first deferred procedure call and the at least one other deferred procedure call  
4           to a resource comprising a multi-processor system; and  
5     execute the first deferred procedure call on one processor of the multi-processor system  
6           while executing the at least one other deferred procedure call on another processor  
7           of the multi-processor system.

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1           25.     (Original) The article of claim 20, wherein the instructions, when  
2     executed, further cause the machine to:  
3     assign the first deferred procedure call to a resource comprising a first processor;  
4     assign the at least one other deferred procedure call to a resource comprising a second  
5           processor; and  
6     execute the first deferred procedure call on the first processor while executing the at least  
7           one other deferred procedure call on the second processor.

1           26.     (Currently Amended) The article of claim 20, wherein the instructions,  
2     when executed, further cause the machine to process ~~another~~ a third interrupt event  
3     associated with the source with the first deferred procedure call, the third interrupt event  
4     comprising a third type of event ~~or the at least one other deferred procedure call.~~

1           27.   (Currently Amended) An article of manufacture comprising:  
2           a machine accessible medium, the machine accessible medium providing instructions  
3           that, when executed by a machine, cause the machine to:  
4           request a first deferred procedure call for a first interrupt event associated with a  
5           source;  
6           request at least one other different deferred procedure call for a second interrupt  
7           event associated with the source, wherein the first interrupt event  
8           comprises one type of event and the second interrupt event comprises  
9           another type of event; and  
10          process the first interrupt event with the first deferred procedure call while  
11          processing the second interrupt event with the at least one other deferred  
12          procedure call.

1           28.   (Original) The article of claim 27, wherein the instructions, when  
2           executed, further cause the machine to:  
3           execute the first deferred procedure call on a first thread of a processor; and  
4           execute the at least one other deferred procedure call on a second thread of the processor.

1           29.   (Original) The article of claim 27, wherein the instructions, when  
2           executed, further cause the machine to:  
3           execute the first deferred procedure call on a first processor; and  
4           execute the at least one other deferred procedure call on a second processor.

1           30.     (Currently Amended) The article of claim 27, wherein the instructions,  
2     when executed, further cause the machine to process ~~another~~ a third interrupt event  
3     associated with the source with the first deferred procedure call, the third interrupt event  
4     comprising a third type of event ~~or the at least one other deferred procedure call.~~

1           31.     (Previously Submitted) The method of claim 1, wherein the source  
2     comprises a peripheral device of a computer system.

1           32.     (Previously Submitted) The method of claim 8, wherein the source  
2     comprises a peripheral device of a computer system.

1           33.     (Previously Submitted) The driver of claim 12, wherein the source  
2     comprises a peripheral device of a computer system.

1           34.     (Previously Submitted) The article of manufacture of claim 20, wherein  
2     the source comprises a peripheral device of a computer system.

1           35.     (Previously Submitted) The article of manufacture of claim 27, wherein  
2     the source comprises a peripheral device of a computer system.